

Amendments to the Claims

This listing of claims will replace all prior versions, and listings, of claims in the application.

1. (Currently amended) A method in a packet switched data transfer system for processing header bits and payload bits in a frame of bits, the method comprising:
 - classifying each of the header bits in the frame into a first predetermined class of bits and into a second predetermined class of bits;
 - classifying each of the payload bits in the frame into the first predetermined class of bits and into the second predetermined class of bits;
 - processing the first predetermined class of bits, in the frame, in accordance with a first predetermined mechanism; and
 - processing the second predetermined class of bits, in the frame, in accordance with a second predetermined mechanism.
2. (Original) The method of claim 1, further comprising:
 - constructing a new frame of bits based upon the processed first predetermined class of bits and the processed second predetermined class of bits.
3. (Original) The method of claim 1, wherein:
 - classifying the header bits into the first predetermined class of bits and into the second predetermined class of bits includes classifying each of the header bits based upon a location of the header bit in the frame of bits, and
 - classifying the payload bits into the first predetermined class of bits and into the second predetermined class of bits includes classifying each of the payload bits based upon a location of the payload bit in the frame of bits.

4. (Original) The method of claim 1, wherein:
classifying the header bits into the first predetermined class of bits and into the second predetermined class of bits includes classifying each of the header bits based upon a pre-assigned header weight of the header bit, and
classifying the payload bits into the first predetermined class of bits and into the second predetermined class of bits includes classifying each of the payload bits based upon a pre-assigned payload weight of the payload bit.
5. (Original) The method of claim 1, wherein:
processing the first predetermined class of bits in accordance with the first predetermined mechanism includes grouping the first predetermined class of bits; and
processing the second predetermined class of bits in accordance with the second predetermined mechanism includes grouping the second predetermined class of bits.
6. (Original) The method of claim 1, further comprising:
grouping the processed first predetermined class of bits;
grouping the processed second predetermined class of bits; and
constructing a new frame of bits based upon the grouped-processed first predetermined class of bits and the grouped-processed second predetermined class of bits.
7. (Original) The method of claim 1, wherein:
the first predetermined mechanism includes applying a first error protection algorithm,
and
the second predetermined mechanism includes applying a second error protection algorithm.

8. (Currently amended) A method in a packet switched data transfer system for reducing an encoded frame size a frame having header bits and payload bits, the method comprising:

classifying each of the header bits in the frame into a first predetermined class of bits and into a second predetermined class of bits;

classifying each of the payload bits in the frame into the first predetermined class of bits and into the second predetermined class of bits;

encoding the first predetermined class of bits, in the frame, in accordance with a first encoding process; and

encoding the second predetermined class of bits, in the frame, in accordance with a second encoding process,

wherein the first encoding process is different from the second encoding process.

9. (Original) The method of claim 8, further comprising:
constructing a new frame based upon the encoded first predetermined class of bits and the encoded second predetermined class of bits.

10. (Original) The method of claim 8, further comprising:
grouping the encoded first predetermined class of bits;
grouping the encoded second predetermined class of bits; and
constructing a new frame based upon the grouped encoded first predetermined class of bits and the grouped encoded second predetermined class of bits.

11. (Original) The method of claim 8, wherein the first predetermined encoding process has a first coding rate greater than a second coding rate of the second predetermined encoding process.

12. (Currently amended) A method in a packet switched data transfer system for reformatting a frame having header bits and payload bits, the method comprising:

classifying each of the header bits in the frame into a first predetermined class of bits and into a second predetermined class of bits;

classifying each of the payload bits in the frame into the first predetermined class of bits and into the second predetermined class of bits;

grouping the classified header bits of the first predetermined class of bits with the classified payload bits of the first predetermined class of bits;

grouping the classified header bits of the second predetermined class of bits with the classified payload bits of the second predetermined class of bits; and

constructing a reformatted frame using the grouped first predetermined class of bits and the grouped second predetermined class of bits.

13. (Original) The method of claim 12, further comprising:

before constructing a reformatted frame,

encoding the grouped first predetermined class of bits with a first predetermined algorithm; and

encoding the grouped second predetermined class of bits with a second predetermined algorithm,

wherein constructing a reformatted frame includes constructing a reformatted frame using the encoded grouped first predetermined class of bits and the encoded grouped second predetermined class of bits.

14. (Original) The method of claim 13, wherein the first predetermined algorithm has a first coding rate greater than a second coding rate of the second predetermined algorithm.

15. (Currently amended) A packet switched data transfer device comprising:
a frame receiver configured to receive a frame of bits, the frame of bits comprising a plurality of header bits and a plurality of payload bits;
a bit classifier coupled to the frame receiver, the bit classifier configured to classify each of the plurality of header bits and each of the plurality of payload bits into a first class of bits and into a second class of bits; and
a bit processor coupled to the bit classifier, the bit processor configured to process the classified first class of bits, in the frame, according to a first predetermined process and to process the classified second class of bits, in the frame, according to a second predetermined process.

16. (Original) The packet switched data transfer device of claim 15, wherein:
the bit classifier is further configured to classify each header bit of the plurality of header bits based upon a location of the header bit in the frame of bits and to classify payload bits of the plurality of payload bits based upon a location of the payload bit in the frame of bits.

17. (Original) The packet switched data transfer device of claim 15, wherein:
the bit classifier is further configured to classify each header bit of the plurality of header bits based upon a pre-assigned header weight of the header bit and to classify each payload bit of the plurality of payload bits based upon a pre-assigned payload weight of the payload bit.

18. (Original) The packet switched data transfer device of claim 15, wherein:
the first predetermined process has a first coding rate, and
the second predetermined process has a second coding rate, the second coding rate less than the first coding rate.

19. (Original) The packet switched data transfer device of claim 15, further comprising:
a frame constructor coupled to the bit processor, the frame constructor configured to construct a new frame of bits based upon the processed first class of bits and the processed second class of bits.